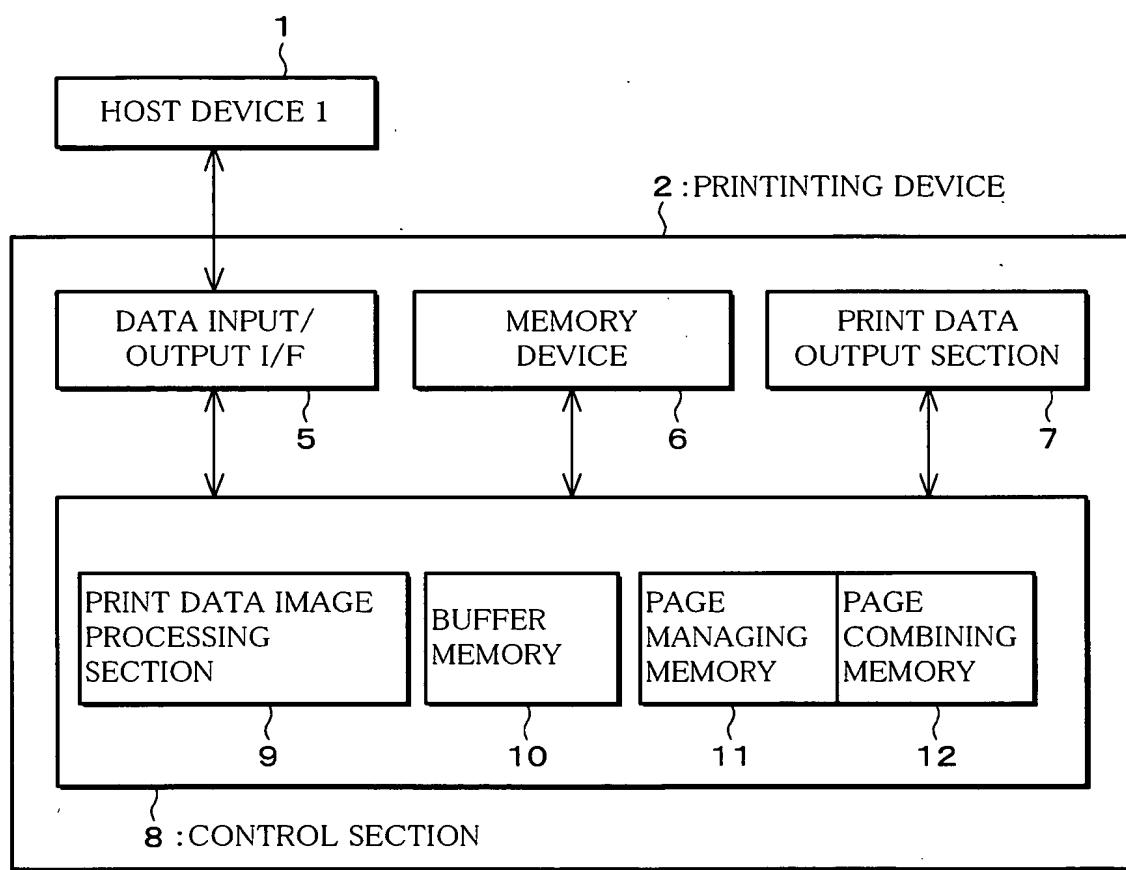
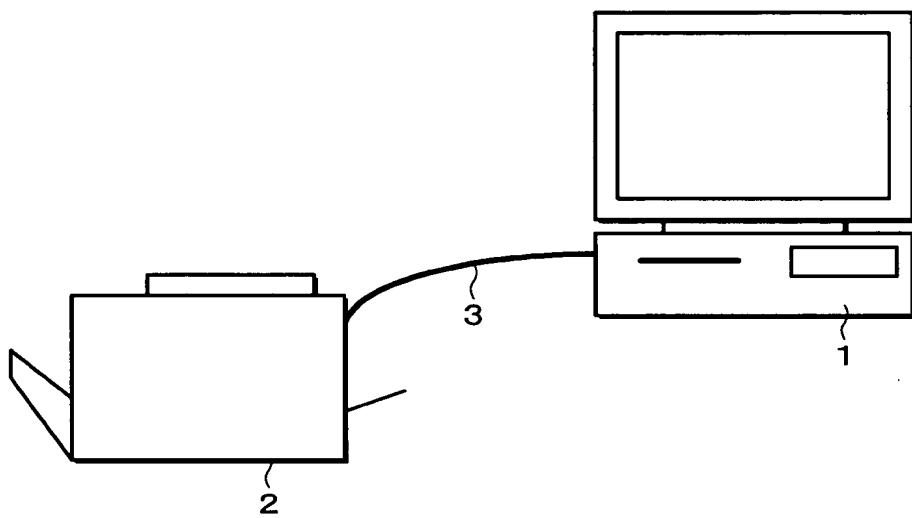


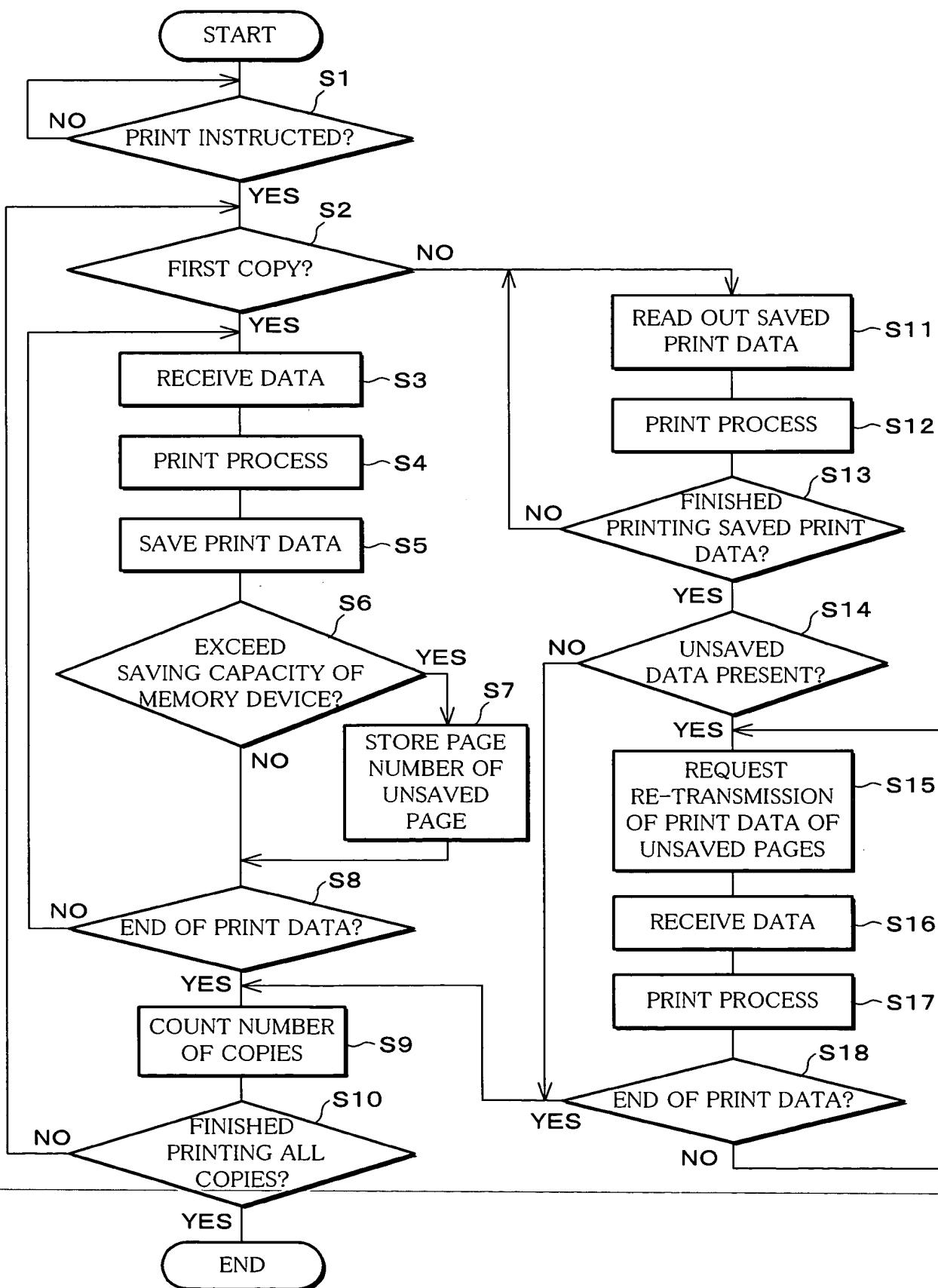
FIG. 1



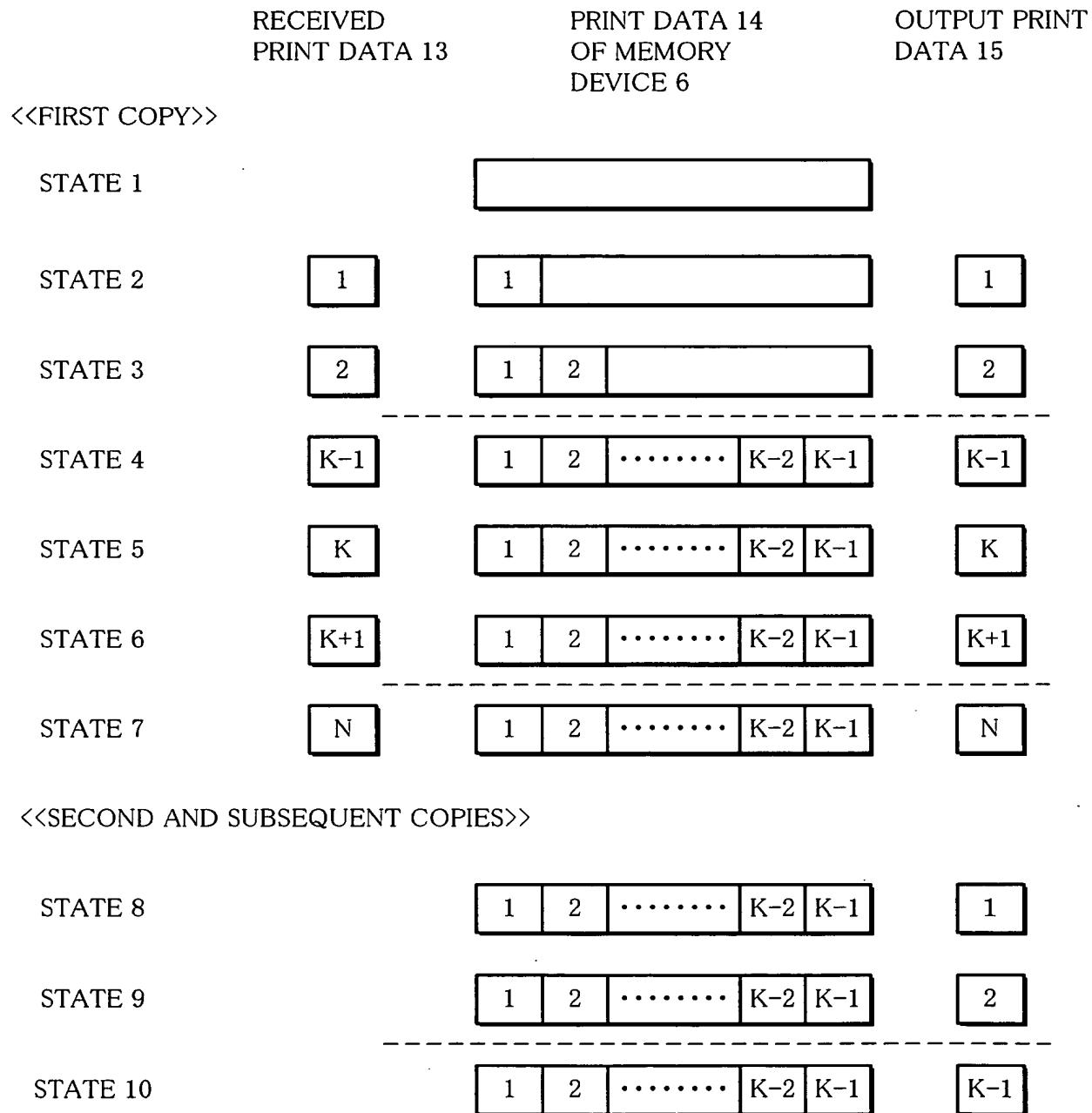
F I G. 2



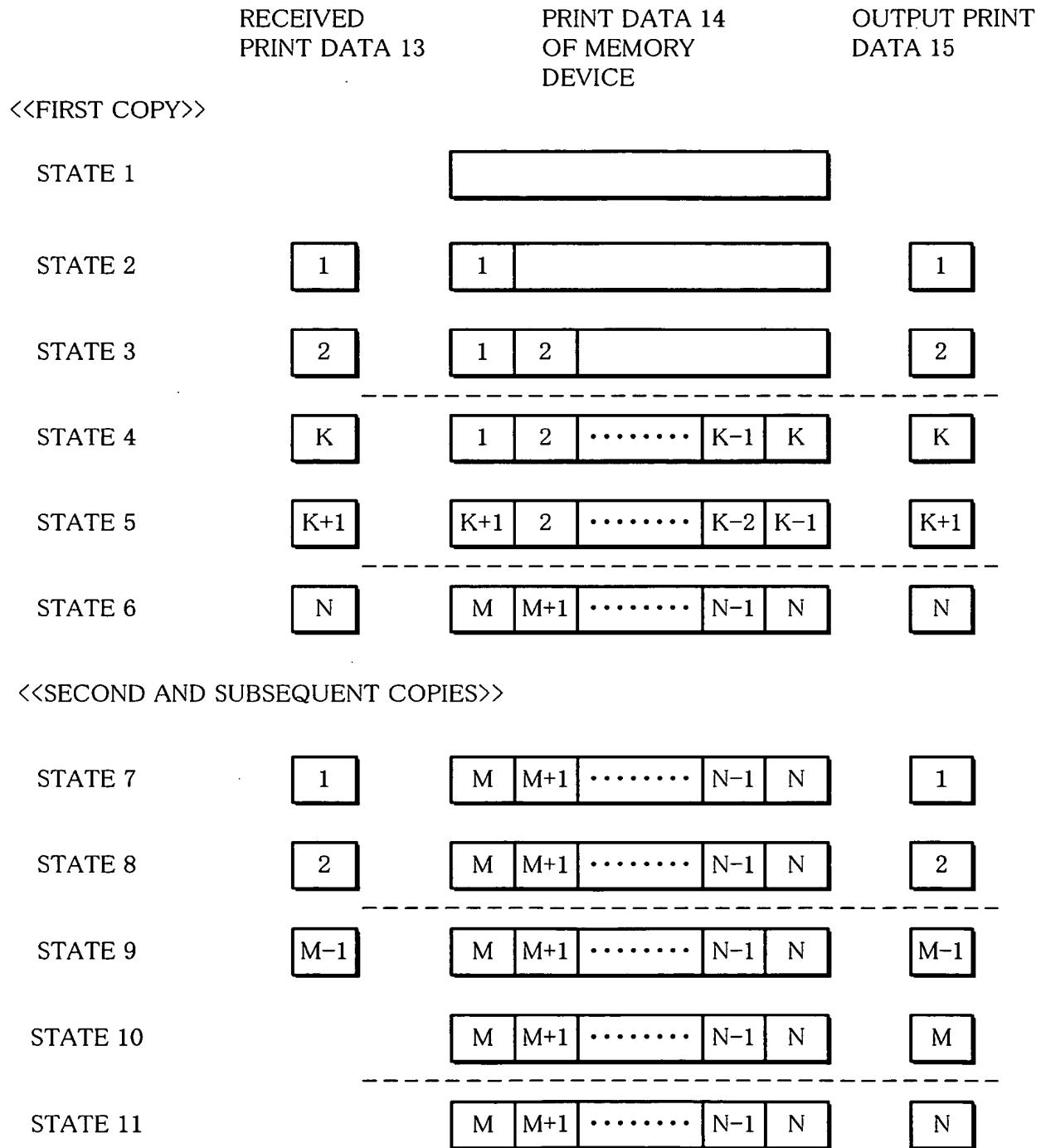
F I G. 3



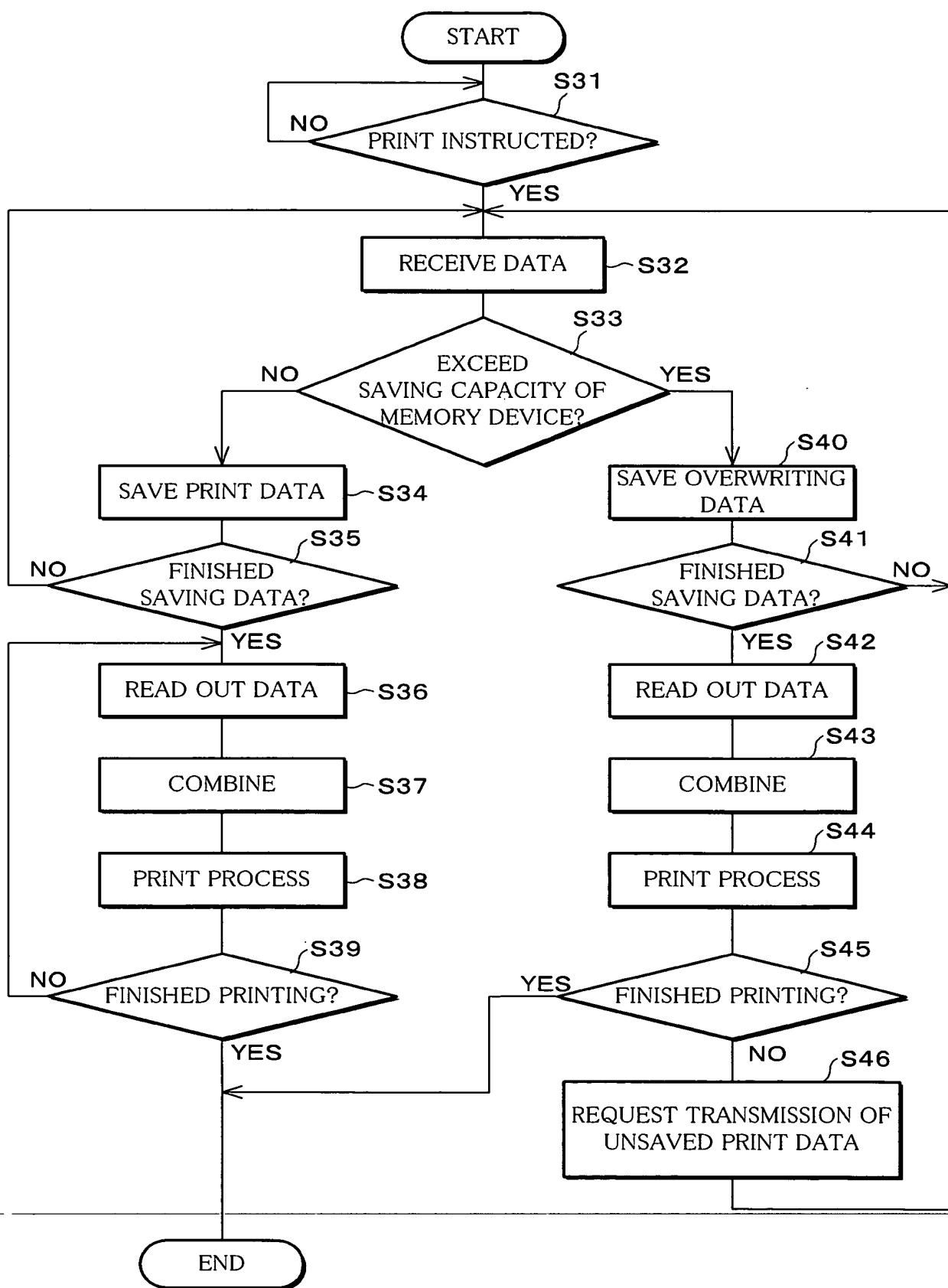
# F I G. 4



# F I G. 5



F I G. 6

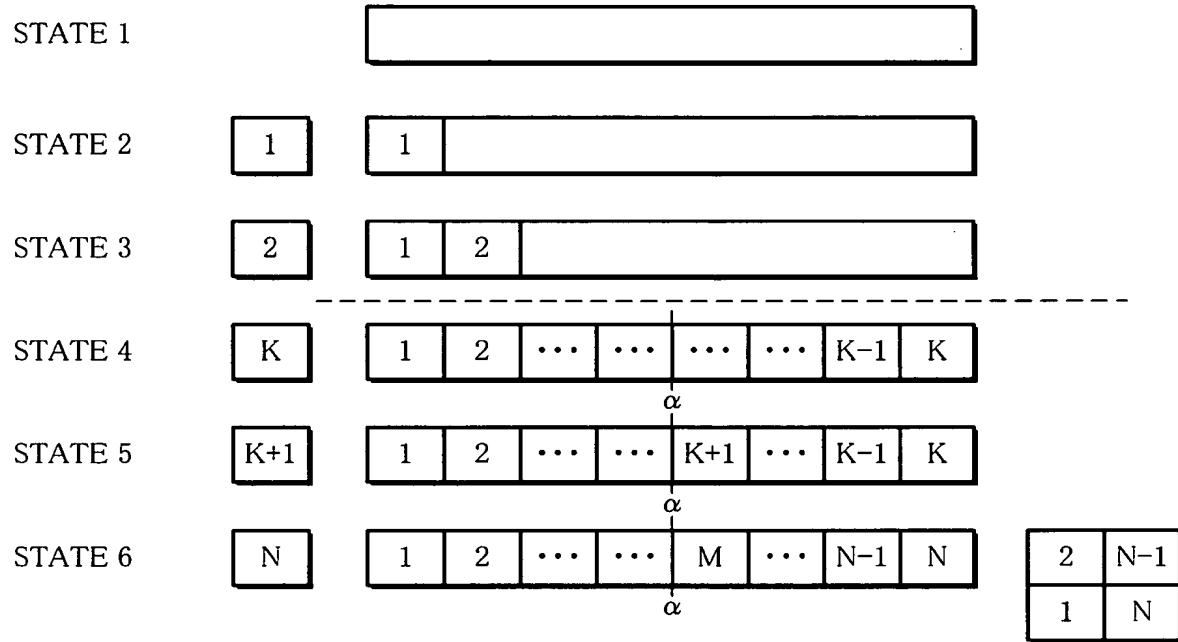


# FIG. 7

RECEIVED  
PRINT DATA 13

PRINT DATA 14  
OF MEMORY  
DEVICE 6

OUTPUT PRINT  
DATA 15



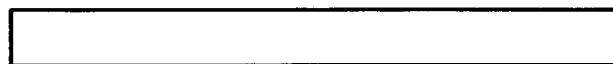
# F I G. 8

RECEIVED  
PRINT DATA 13

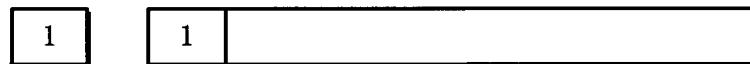
PRINT DATA 14  
OF MEMORY  
DEVICE

OUTPUT PRINT  
DATA 15

STATE 1



STATE 2



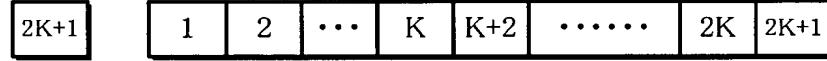
STATE 3



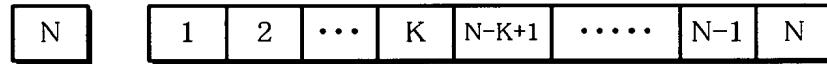
STATE 4



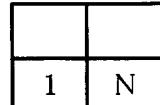
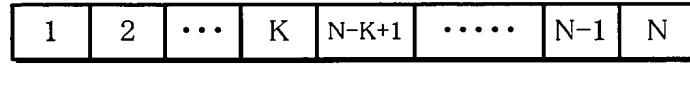
STATE 5



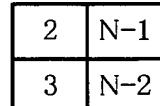
STATE 6



STATE 7



STATE 8



STATE 9

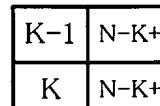


FIG. 9 (a)

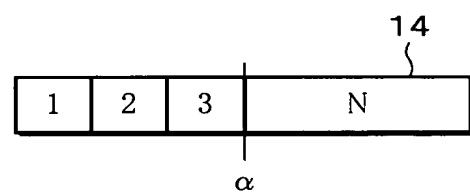


FIG. 9 (a)

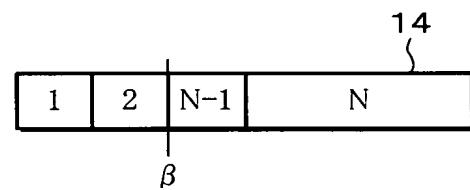
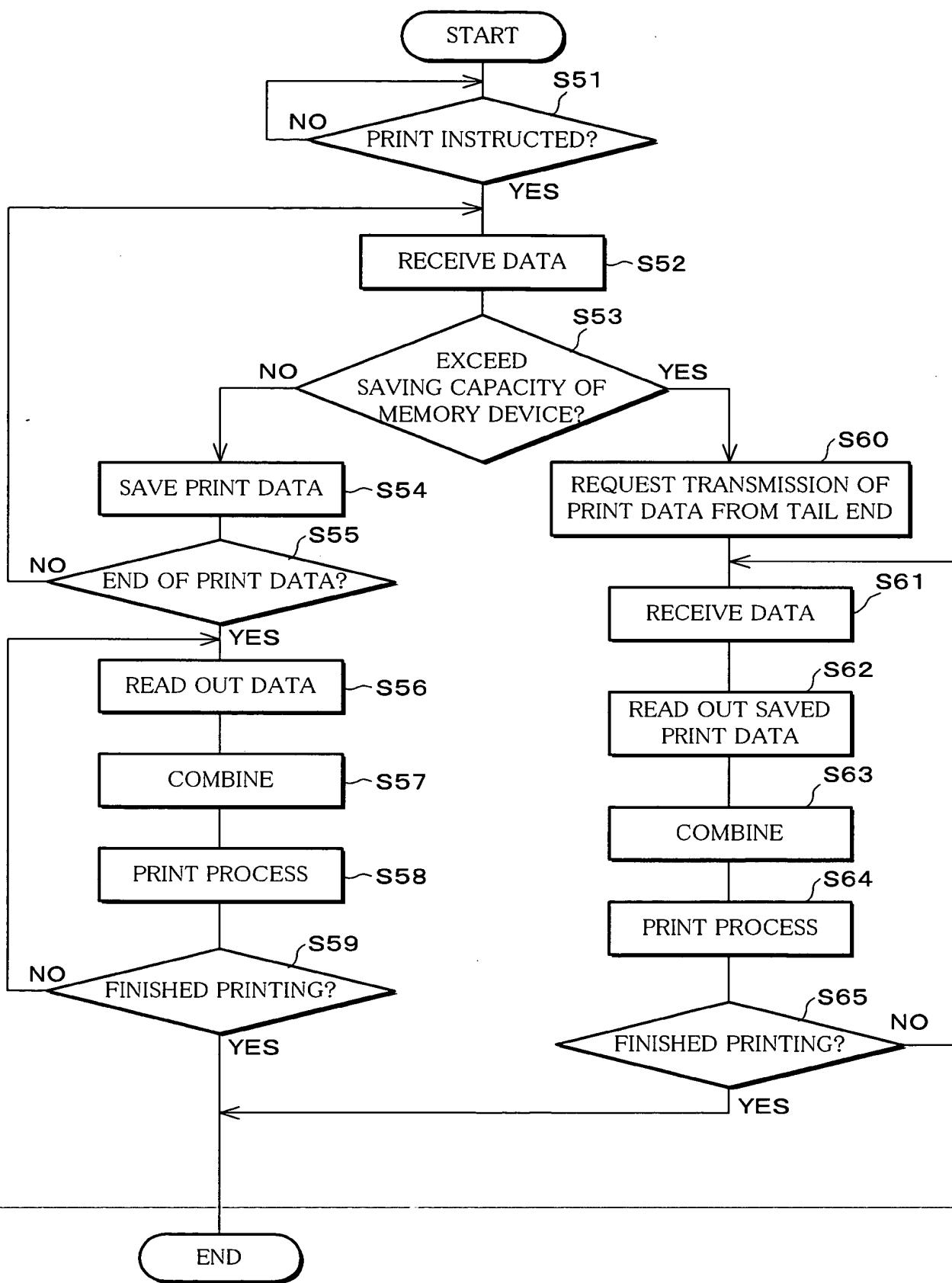


FIG. 10

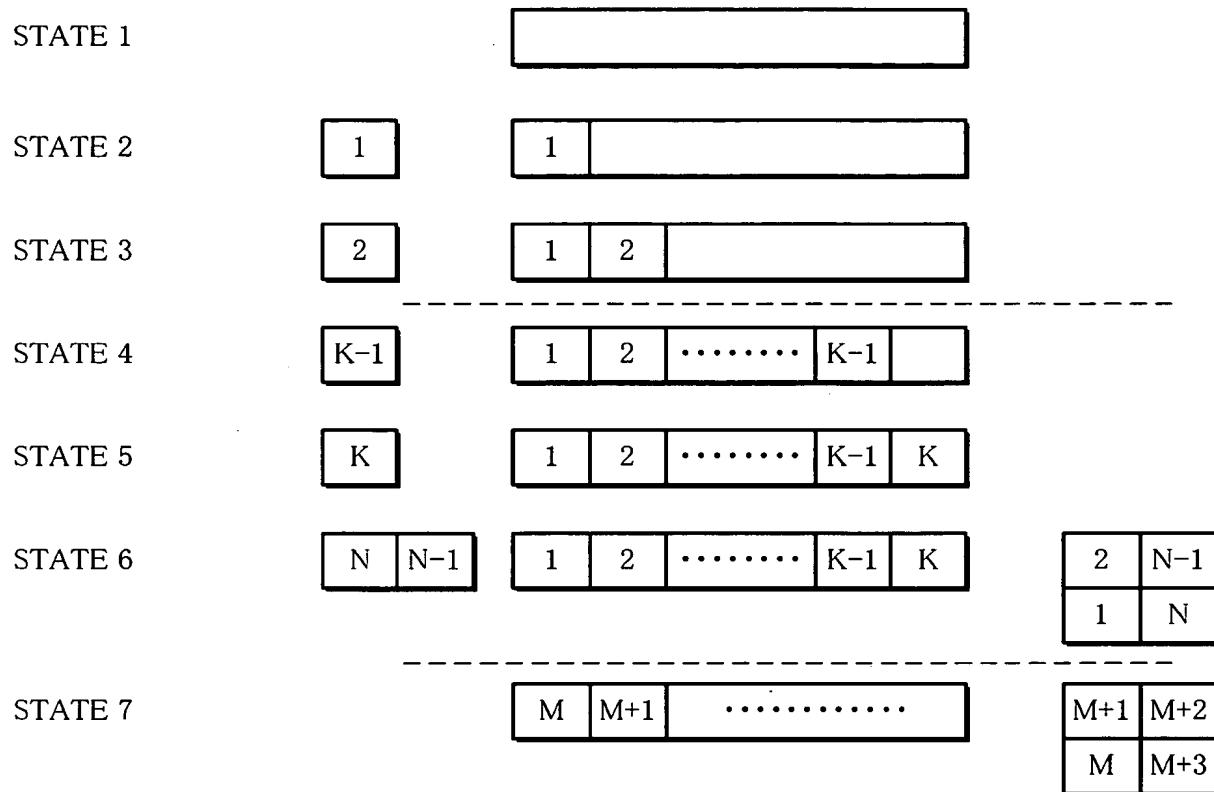


### FIG. 11

RECEIVED  
PRINT DATA 13

PRINT DATA 14  
OF MEMORY  
DEVICE

OUTPUT PRINT  
DATA 15



## FIG. 12

